

INFORMATION DISCLOSURE STATEMENT BY APPLICANT (Not for submission under 37 CFR 1.99)	Application Number		10550323
	Filing Date		2005-09-23
	First Named Inventor	Roger D. Chamberlain	
	Art Unit	2162	
	Examiner Name	Fleurantin, Jean B.	
	Attorney Docket Number	53047-57370	

U.S.PATENTS

Examiner Initial*	Cite No	Patent Number	Kind Code ¹	Issue Date	Name of Patentee or Applicant of cited Document	Pages, Columns, Lines where Relevant Passages or Relevant Figures Appear
	1	5704060		1997-12-30	Del Monte	
	2	6870929	B1	2005-03-22	Greene	
	3	7362859	B1	2008-04-22	Robertson et al.	
	4	7623660	B1	2009-11-24	Cory	

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U.S.PATENT APPLICATION PUBLICATIONS

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	1	20010033656	A1	2001-10-25	Gligor et al.	
	2	20020103663		2002-08-01	Bankier et al.	

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3	20020181709	A1	2002-12-05	Sorimachi et al.	
4	20030169877		2003-09-11	Liu et al.	
5	20040117645		2004-06-17	Okuda et al.	
6	20060059213		2006-03-16	Evoy	
7	20080260158	A1	2008-10-23	Chin et al.	

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	1	Notice of Allowance for US Application 11/339892 dated January 20, 2011	<input type="checkbox"/>

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2	Notice of Allowance for US Application 11/359,285 dated December 8, 2010	<input type="checkbox"/>
3	Notice of Allowance for US Application 11/765,306 dated January 25, 2011	<input type="checkbox"/>
4	Notice of Allowance for US Application 11/932391 dated January 19, 2011	<input type="checkbox"/>
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6	Notice of Allowance for US Application 12/703,388 dated January 3, 2011	<input type="checkbox"/>
7	Office Action for US Application 10/550326 dated December 23, 2010	<input type="checkbox"/>
8	Office Action for US Application 11/690034 dated December 14, 2010	<input type="checkbox"/>
9	Search Results from IEEE Xplore for "Deterministic Finite Automaton current states", dated 12/28/10, 2 pages, citing COLE, "Real-Time Computation by n-Dimensional Iterative Arrays of Finite-State Machines", 1966; KASHYAP, "Syntactic Decision Rules for Recognition of Spoken Words and Phrases Using a Stochastic Automaton", 1979; ROUVELLOU ET AL., "Inference of a Probabilistic Finite State Machine from its Output", 1995; and YI ET AL., "A Method of Instance Learning based on Finite-State Automaton and its Application on TCM Medical Cases", 2010, et al.	<input type="checkbox"/>
10	TAU ET AL., "Transit Note #114: A First Generation DPGA Implementation", January 1995, 9 pages	<input type="checkbox"/>
11	YAMAGUCHI ET AL., "An Approach for Homology Search with Reconfigurable Hardware", Google, 2001, pp. 374-375	<input type="checkbox"/>

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EXAMINER SIGNATURE

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